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LISTING OF CLAIMS:

1. (Previously presented): A liquid crystal display panel, comprising:

a substrate;

a first set of electrode layers;

a second set of electrode layers, wherein the first set of the electrode layers comprises electrodes supported by the substrate and the second set of electrode layers comprises electrodes supported by the same substrate;

a thin film transistor, wherein only one of the first and second set of electrode layers is operatively coupled to the thin film transistor to correspond to an active matrix mode, and the other one of the first and second set of electrode layers corresponds to a passive matrix mode, wherein the first set of the electrode layers or the second set of the electrode layers are activated optionally to create images.

2. (Previously presented): A liquid crystal display, comprising:

a panel, comprising:

a substrate;

a first set of electrode layers with;

a second set of electrode layers, wherein the first set of the electrode layers comprises electrodes supported by the substrate and the second set of electrode layers comprises electrodes supported by the same substrate;

a thin film transistor, wherein only one of the first and second set of electrode layers is operatively coupled to the thin film transistor to correspond to an active matrix mode, and the other one of the first and second set of electrode layers corresponds to a passive matrix mode,

wherein the first set of the electrode layers or the second set of the electrode layers are activated optionally to create images.

3. (Previously presented): A reflection type liquid crystal display having at least two matrix modes converged within a panel, comprising:

a first substrate;

a second substrate opposite to said first substrate, wherein opposing inner surfaces of said first substrate and said second substrate have a plurality of thin film transistors and a color filter fabricated thereon respectively;

a first insulating layer formed on said first substrate;

a reflective layer formed on said first insulating layer to reflect and diffuse the incident light entering from said second substrate;

a second insulating layer formed on said reflective layer;

a first lower electrode formed on said second insulating layer;

a first upper electrode formed on said color filter;

a third insulating layer formed on said first lower electrode;

a fourth insulating layer formed on said first upper electrode; and

a second lower electrode formed on said third insulating layer,

wherein only one of the first lower electrode and the second lower electrode is operatively coupled to one of the plurality of thin film transistors to correspond to an active matrix mode, and the other one of the first lower electrode and the second lower electrode corresponds to a passive matrix mode.

4. (Original): The reflection type liquid crystal display of claim 3, wherein said first upper electrode is served as a common electrode for said first lower electrode and said second lower electrode.

5. (Original): The reflection type liquid crystal display of claim 3, further comprising a second upper electrode located on said fourth insulating layer.

6. (Original): The reflection type liquid crystal display of claim 3, wherein said reflective layer is perforated.

7. (Original): The reflection type liquid crystal display of claim 3, wherein a thickness of said reflective layer is ranging from 50 to 1000 angstroms.

8. (Original): The reflection type liquid crystal display of claim 3, wherein said first lower electrode and said second lower electrode are staggered.

9. (Original): The reflection type liquid crystal display of claim 3, wherein said second lower electrode is supplied with a voltage to neutralize a parasitic voltage induced at the time of activating said first lower electrode.

10-23. (Canceled)

24. (Previously presented): The liquid crystal display panel as in claim 1, further comprising a liquid crystal layer, wherein the first set of electrode layers comprises a first pair of electrodes operating on the liquid crystal layer, wherein the second set of electrode layers comprises a second pair of electrodes operating on the same liquid crystal layer, and wherein the first pair of the electrodes or the second pair of electrodes are selectively operated to create images with the same liquid crystal layer.

25. (Previously presented): The liquid crystal display panel as in claim 24, wherein the first pair of electrodes comprises a first upper electrode and a first lower electrodes, and the second pair of electrodes comprises a second upper electrode and a second lower electrode, wherein at least the first and second upper electrodes or the first and second lower electrodes are stacked in layers on a same side of the liquid crystal layer, without any liquid crystal layer between the stacked layers.

26. (Previously presented): The liquid crystal display panel as in claim 24, wherein the first pair of electrodes comprises a first upper electrode and a first lower electrodes, and the second pair of electrodes comprises a second upper electrode and a second lower electrode, wherein at least the first and second upper electrodes or the first and second lower electrodes are staggered on a same side of the liquid crystal layer.

27. (Previously presented): The liquid crystal display panel as in claim 24, further comprising a reflective layer formed on one side of the liquid crystal layer to reflect or diffuse ambient light through the liquid crystal layer.

28. (Currently amended): The liquid crystal display as in claim + 2, wherein the panel further comprising a liquid crystal layer, wherein the first set of electrode layers comprises a first pair of electrodes operating on the liquid crystal layer, wherein the second set of electrode layers comprises a second pair of electrodes operating on the same liquid crystal layer, and wherein the first pair of the electrodes or the second pair of electrodes are selectively operated to create images with the same liquid crystal layer.

29. (Previously presented): The liquid crystal display as in claim 28, wherein the first pair of electrodes comprises a first upper electrode and a first lower electrodes, and the second pair of electrodes comprises a second upper electrode and a second lower electrode, wherein at least the first and second upper electrodes or the first and second lower electrodes are stacked in layers on a same side of the liquid crystal layer, without any liquid crystal layer between the stacked layers.

30. (Previously presented): The liquid crystal display as in claim 28, wherein the first pair of electrodes comprises a first upper electrode and a first lower electrodes, and the second pair of electrodes comprises a second upper electrode and a second lower electrode, wherein at least the first and second upper electrodes or the first and second lower electrodes are staggered on a same side of the liquid crystal layer.

31. (Previously presented): The liquid crystal display as in claim 28, further comprising a reflective layer formed on one side of the liquid crystal layer to reflect or diffuse ambient light through the liquid crystal layer.

32. (Previously presented): The liquid crystal display as in claim 3, further comprising a liquid crystal layer, wherein the first upper electrode is disposed on one side of the liquid crystal layer, and the first lower electrode and second lower electrode are disposed on another side of the liquid crystal layer, wherein the first upper electrode, the first lower electrode and the second lower electrode operate on the same liquid crystal layer, and wherein the first lower electrode or the second lower electrode is selectively operated to create images with the same liquid crystal layer.

33. (Previously presented): The liquid crystal display as in claim 32, wherein the first and second lower electrodes are stacked in layers on said another side of the liquid crystal layer, without any liquid crystal layer between the stacked layers.

34. (Previously presented): The liquid crystal display as in claim 32, wherein the first and second lower electrodes are staggered on said another side of the liquid crystal layer.

35. (Previously presented): A liquid crystal panel, comprising:
a liquid crystal layer, having a first side and a second side;
a first electrode disposed on the first side of the liquid crystal layer, wherein the first electrode corresponds to an active matrix mode;
a second electrode disposed on the same first side of the liquid crystal layer, wherein the second electrode corresponds to a passive matrix mode, and wherein there is no liquid crystal layer between the first and second electrodes;
at least a third electrode disposed on the second side of the liquid crystal layer,

wherein the first and second electrode selectively operate with the third electrode, to create images with the same liquid crystal layer.

36. (Canceled)

37. (Previously presented): The liquid crystal panel as in claim 35, wherein the first and second electrodes are stacked in layers on the first side of the liquid crystal layer, without any liquid crystal layer between the stacked layers.

38. (Previously presented): The liquid crystal panel as in claim 35, wherein the first and second electrodes are staggered on the first side of the liquid crystal layer.

39. (Previously presented): The liquid crystal panel as in claim 35, further comprising a reflective layer formed on the first side of the liquid crystal layer to reflect or diffuse ambient light through the liquid crystal layer.

40. (Previously presented): The liquid crystal panel as in claim 39, wherein the reflective layer is perforated.

41. (Canceled)

42. (Previously presented): A liquid crystal panel comprising:
a liquid crystal layer, having a first side and a second side;
a first electrode disposed on the first side of the liquid crystal layer;

a second electrode disposed on the same first side of the liquid crystal layer;
at least a third electrode disposed on the second side of the liquid crystal layer,
a substrate and a thin film transistor disposed on the substrate, wherein only one of the first and second electrodes is operatively coupled to the thin film transistor to correspond to an active matrix mode, and the other one of the first and second electrodes corresponds to a passive matrix mode wherein the first and second electrode selectively operate with the third electrode, to create images with the same liquid crystal layer.